

Amendments to the Description

Please replace the paragraph that begins at page 8, line 16, with the following amended paragraph:

The absolute level frequency response may also be combined with the frequency sweep results of ordinary analog channel responses to obtain a ~~wide band~~ wideband frequency response. To this end, the absolute level frequency response may be combined with results from a conventional non-invasive analog sweep tester to provide an overall system response for a system that includes analog and digital channels. Alternatively, the measurement device **10** may itself be modified to also perform analog sweep testing. To this end, the measurement circuit **24** may be modified to include analog television measurement functionality such as that described in U.S. Patent 5,585,842, which is incorporated herein by reference.

Please replace the paragraph that begins at page 11, line 6, with the following amended paragraph:

The tuner **140** further provides raw signal strength information on signal line **141** to the processor **100**. The raw signal strength information may suitably be instantaneous amplitude information such as that produced by a log amp detector or the like. The processor **100** converts the raw signal strength information into absolute signal strength measurements for the received digital channel. Such absolute signal strength measurements are also used to calibrate the tuner output based on a predetermined reference for each channel. The processor **100** may generate absolute signal strength measurements using any known technique for generating absolute signal strength measurements of a digital television signal, including those taught in U.S. Patent No. 6,041,076 to Franchville, et al., U.S. Patent Application Serial Number 09/259,508 to Chappell, now abandoned, or U.S. Patent Application No. 09/282,735 to Bowyer, now abandoned, all of which are incorporated herein by

reference.

Please replace the paragraph that begins at page 11, line 18, with the following amended paragraph:

FIG. 3 shows in further detail an exemplary embodiment of the tuner **140** of FIG. 2. The tuner **140** employs IF bandwidth filters so that the IF signal has a 6 MHz bandwidth, as is well-known, e.g., Triad manufactures such a tuner for cable modems and settop boxes. The exemplary tuner **140** ~~of~~ includes a two stage mixing arrangement where a variable voltage-controlled oscillator (VCO) **145** generates a frequency for mixing with the incoming broadband signal in order to produce a predetermined first IF frequency at approximate 44.5 MHz. The 44.5 MHz. frequency is chosen for convenience and for the benefit of using commercially available component, such as the bandpass filter **149**.

Please replace the paragraph that begins at page 14, line 14, with the following amended paragraph:

The processor **100** also receives the tap weight coefficients or tap weights from the update mechanism **101**. The tap weight coefficients, as they exist once the equalizer **170** acquires lock, represent the inverse of the response of the channel in the time domain. The processor **100** transforms the tap weights into a relative frequency response for the channel by performing a Fast Fourier Transform (FFT)~~an FFT~~. The processor **100** then adds the relative frequency response data to the absolute signal strength result based on the raw signal strength information in order to generate the absolute level frequency response.

Please replace the paragraph that begins at page 15, line 23, with the following amended paragraph:

After step **202**, step **203** is performed. At step **203**, the attenuator parameters are adjusted and recorded before or while the equalizer ~~**206**~~ acquires lock in step **206**. It is noted that the steps **202**, **203**, **204**, **206**, and **207** can each be repeated as necessary in an iterative process, so that the single blocks shown in **FIG. 6** can each represent a number of cycles or iterations rather than merely single steps. In addition, groups of two or more steps can be performed as nested loops (not shown).